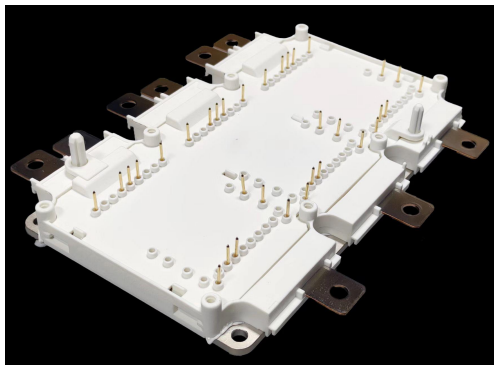




Features

- High Temperature, Humidity, and Bias Operation
- Ultra Low Loss <10nH
- High-Frequency Operation
- Zero Turn-off Tail Current from MOSFET
- Normally-off, Fail-safe Device Operation
- Ease of Paralleling
- Cu Baseplate and Aluminum Nitride Insulator
- Isolated voltage to DC 4200 V

System Benefits

- Enables Compact, Lightweight, Efficient Systems
- Harsh Outdoor Environment Installation
- Mitigates Over-voltage Protection
- Reduced Thermal Requirements
- Reduced System Cost

Package 154.5mm x 126.5mm x 32mm

Part Number	Package	Marking
JX400N1200MK2	MK2	JX400N1200MK2

Absolute Maximum Ratings (T_C = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage	1200	V
V _{GS}	Gate-Source Voltage(dynamic)	-10/+25	V
I _D	Drain Current	400	A
I _{DM}	Drain Current (pulsed)	800	A
P _D	Power Dissipation T _C = 25°C	1250	W
T _C ,T _{stg}	Operating and Storage Temperature Range	-40 to +175	°C
T _J	Junction Temperature	175	°C
L _{Stray}	Stray Inductance	8.5	nH
V _{ISOL}	Isolation test voltage(DC 2mA,t= 10s)	4.2	kV
<i>M</i>	Mounting torque for modul mounting(Screw M4 baseplate to heatsink)	2.0	Nm

**JX400N1200MK2****Electrical Characteristics (T_C = 25°C unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DS}	Drain-source Breakdown Voltage	V _{GS} =0V	1200			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 1200V, V _{GS} = 0V			400	uA
I _{GSS}	Gate-body Leakage Current	V _{GS} =20V, V _{DS} =0V			4	uA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =40mA	2.0		4.0	V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} =20V, I _D =200A		4.2	5.5	mΩ
R _G	Gate Resistance	V _{GS} =0V, f= 1MHz		3.0		Ω
C _{iss}	Input Capacitance	V _{DS} =800V, f= 100kHz, V _{AC} =25mV		19.1		nF
C _{oss}	Output Capacitance			0.49		
C _{rss}	Reverse Transfer Capacitance			100		pF
Q _{GS}	Gate-Source Charge	V _{DD} = 1000V, V _{GS} =-5/+20V I _D =300A,		306.8		nC
Q _{GD}	Gate-Drain Charge			313.2		
Q _G	Total Gate Charge			952		
t _{d(on)}	Turn-on delay time	V _{DD} =600V, V _{GS} =-5/+20V I _D =300A, R _{G(ext)} =5Ω Load=77uH, T _J = 150°C		76		nS
t _r	Rise Time			69		
t _{d(off)}	Turn-off delay time			165		
t _f	Fall Time			42		
V _{sd}	Diode Forward voltage	I _f =300A, V _{GS} =0			6	V

Typical Performance-Reverse Diode(T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
VFSD	Forward Voltage	V _{GS} = 0V, I _F = 200A, T _J = 25°C			6	V
I _S	Continuous Diode Forward Current	V _{GS} = 0V, T _J = 25°C		200		A
I _{Spulse}	Pulsed body diode current			400		A
trr	Reverse Recovery Time	V _{GS} = -5V, I _F = 200A,		98		ns
Qrr	Reverse Recovery Time	V _R =800V, di/dt=3.6kA/μs		306		nC
Irrm	Peak Reverse Recovery Current	T _J = 25°C		52		A

Thermal Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
R _{θJC}	Thermal Resistance, Junction to cooling fluid	T _C =65°C			0.12	°C/W

NTC-Thermistor

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
R ₂₅	Rated resistance	T _C =25°C		5.00		kΩ
ΔR/R	Deviation of R ₁₀₀	T _C = 100°C, R ₁₀₀ = 477Ω	-5		5	%
B _{25/50}	B-value	R ₂ = R ₂₅ exp [B _{25/50} (1/T ₂ - 1/(298,15 K))]		3380		k
B _{25/80}	B-value	R ₂ = R ₂₅ exp [B _{25/80} (1/T ₂ - 1/(298,15 K))]		3468		k
B _{25/100}	B-value	R ₂ = R ₂₅ exp [B _{25/100} (1/T ₂ - 1/(298,15 K))]		3523		k

Typical Performance

Figure 1. Output Characteristics for Various T_J

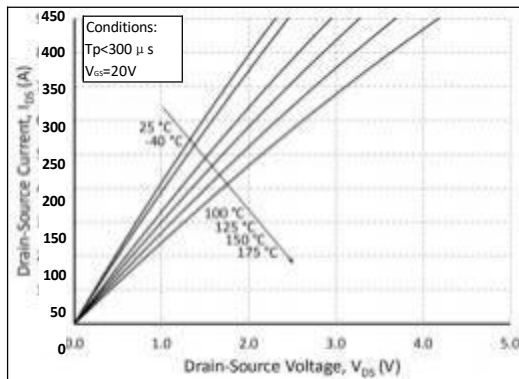


Figure 2. Normalized On-Resistance vs. Temperature

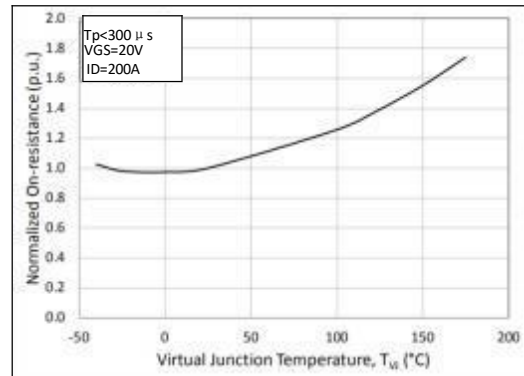


Figure 3. Threshold Voltage vs. Temperature

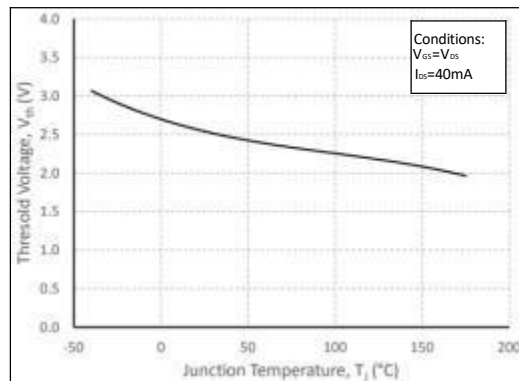


Figure 4. Transfer Characteristic for Various T_J

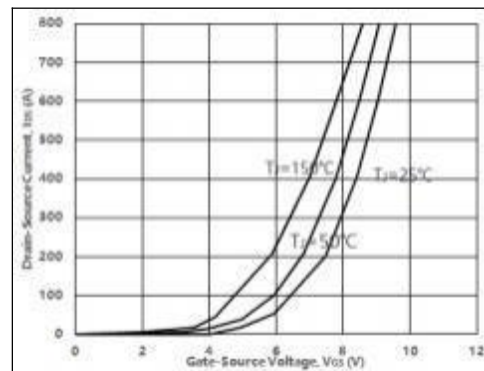


Figure 5. Diode Characteristic at 25 °C

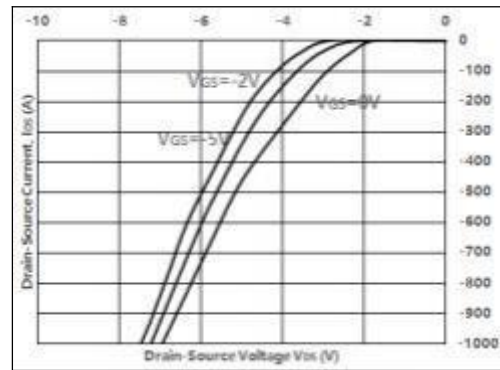


Figure 6. Typical Gate Charge Characteristics

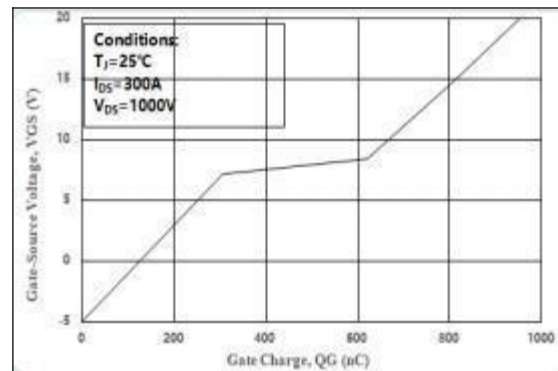


Figure 7. Typical Capacitances vs. Drain-Source Voltage

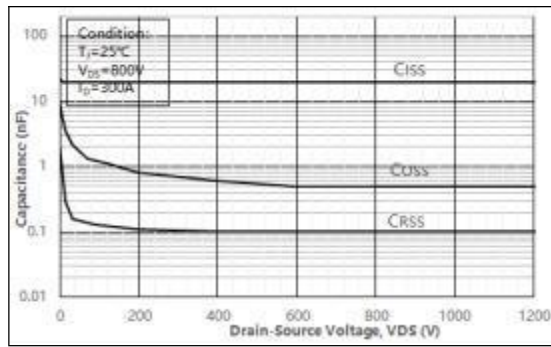


Figure 8. Inductive Switching Energy vs. Drain Current

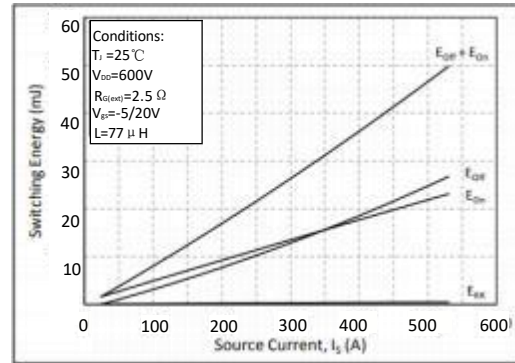


Figure 9. Resistive Switching Time Description

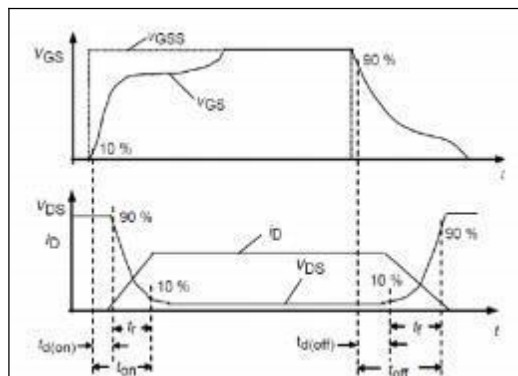
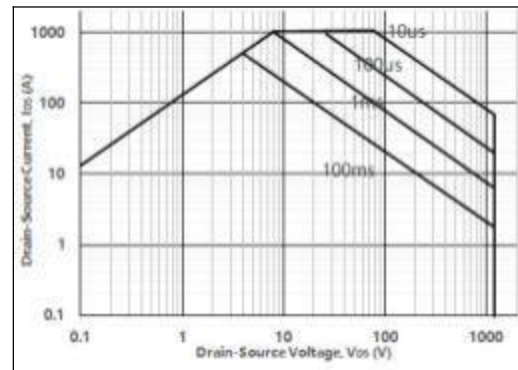
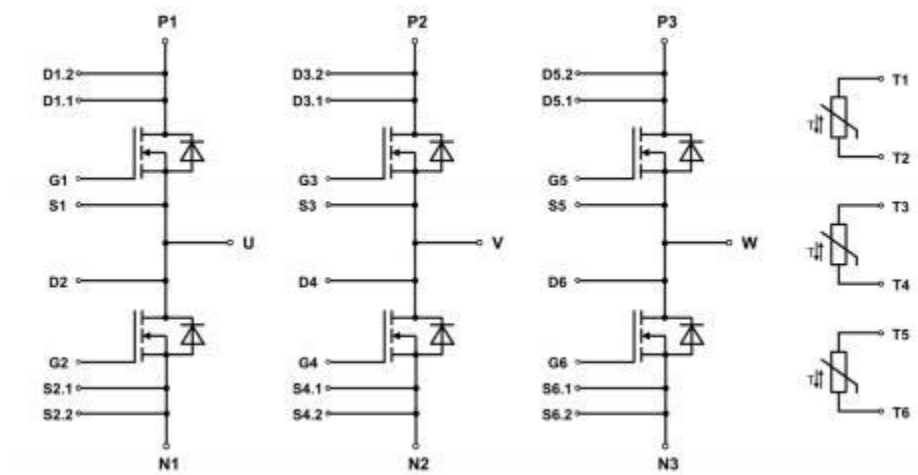


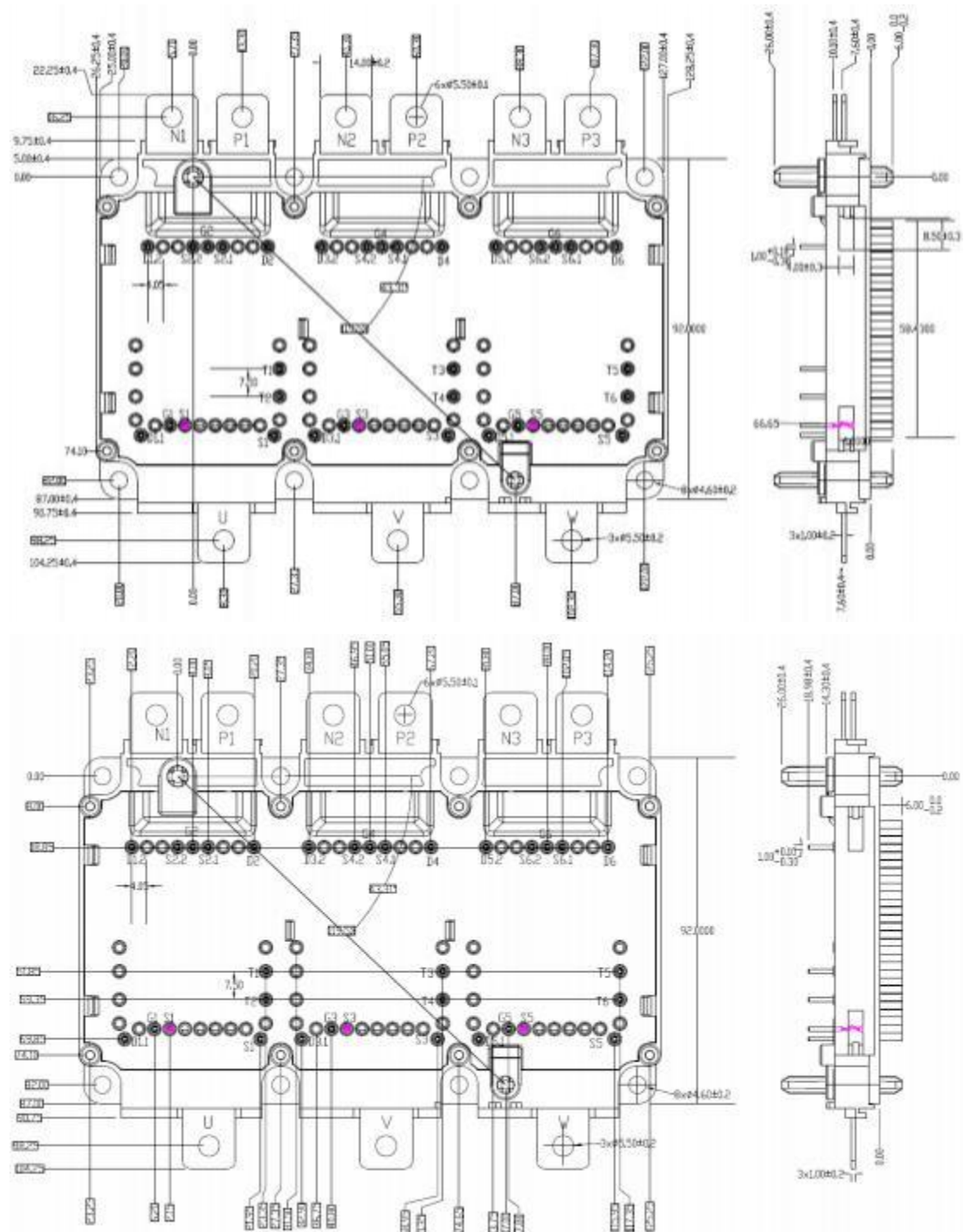
Figure 10. Safe Operating Area



Circuit Diagram Headline



Package Drawing:



未标注线性公差按 GB/1804-2000c级执行	公差分段	0.5-3	3-6	6-30	30-120	120-400
	c级	±0.2	±0.3	±0.5	±0.8	±1.2